

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1. (Canceled)

2. (Currently amended) A driving method for a plasma display panel having an address electrode, a first display electrode formed on said address electrode, a second display electrode formed on a surface opposing to said the first display electrode, and a partition wall formed between said first display electrode and said second display electrode and including a metal electrode therein, comprising ~~the following steps:~~

~~a first step for conducting an~~ addressing operation for each sub-field; and

~~a second step for conducting a~~ sustaining operation for display upon basis of a result of said addressing, wherein,

al cont | in conducting said sustaining operation ~~second step~~, onto said second display electrode is applied pulse voltage differing in polarity and nearly in synchronism with sustain pulse voltage following after second one to be applied onto said first display electrode, thereby forming space charges generated after discharge between said first display electrode and said metal electrode in form of wall charges on said second display electrode.

3. (Currently amended) A driving method for a plasma display panel having an address electrode, a first display electrode formed on said address electrode, a second display electrode formed on a surface opposing to said the first display electrode, and a partition wall formed between said first display electrode and said second display electrode and including a metal electrode therein, comprising ~~the following steps:~~

~~a first step for conducting an~~ addressing operation for each sub-field; and

~~a second step for conducting a~~ sustaining operation for display upon basis of a result of said addressing, wherein,

in conducting said sustaining operation ~~second step~~, onto said first display electrode is applied pulse voltage differing in polarity and ^{and amplified.} nearly in synchronism with first sustain pulse voltage to be applied onto said second display electrode, thereby forming space charges generated after discharge between said second display electrode and said metal electrode in form of wall charges on said first display electrode.

4. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 1, wherein in conducting said sustaining operation ~~second step~~, onto said address electrode is applied short pulse voltage, being different in polarity, at a time earlier than rise-up of the first sustain pulse voltage to be applied onto said first display electrode.

5. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 2, wherein in conducting said sustaining operation ~~second step~~, onto said address electrode is applied short pulse voltage, being different in polarity, at a time earlier than rise-up of the first sustain pulse voltage to be applied onto said first display electrode.

6. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 3, wherein in conducting said sustaining operation ~~second step~~, onto said address electrode is applied short pulse voltage, being different in polarity, at a time earlier than rise-up of the first sustain pulse voltage to be applied onto said first display electrode.

7. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 1, wherein in conducting said sustaining operation ~~second step~~, nearly in synchronism with the sustain pulse voltage to be applied onto said first display electrode, onto said address electrode is applied pulse voltage, being same in polarity, for reducing an influence of capacity between said address electrode upon said first display electrode.

8. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 2, wherein in conducting said sustaining operation ~~second step~~, nearly in synchronism with the sustain pulse voltage to be applied onto said first display electrode, onto said address electrode is applied pulse voltage, being same in polarity, for reducing an influence of capacity between said address electrode upon said first display electrode.

9. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 3, wherein in conducting said sustaining operation ~~second step~~, nearly in synchronism with the sustain pulse voltage to be applied onto said first display electrode, onto said address electrode is applied pulse voltage, being same in polarity, for reducing an influence of capacity between said address electrode upon said first display electrode.

10. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 1, wherein in conducting said addressing operation ~~first step~~, said address electrode and said first display electrode are formed on a same plane, and the address pulse voltage onto said address electrode upon basis of a picture signal and scan pulse voltage onto said first display electrode are applied nearly in synchronism therewith, so as to remove the wall charge formed in advance on both the electrodes without accompanying luminous discharge, thereby selecting a non-luminous cell.

11. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 2, wherein in conducting said addressing operation ~~first step~~, said address electrode and said first display electrode are formed on a same plane, and the address pulse voltage onto said address electrode upon basis of a picture signal and scan pulse voltage onto said first display electrode are applied nearly in synchronism therewith, so as to remove the wall charge formed in advance on both the electrodes without accompanying luminous discharge, thereby selecting a non-luminous cell.

12. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 3, wherein in conducting said addressing operation ~~first step~~, said address electrode and said first display electrode are formed on a same plane, and the address pulse voltage onto said address electrode upon basis of a picture signal and scan pulse voltage onto said first display electrode are applied nearly in synchronism therewith, so as to remove the wall charge formed in advance on both the electrodes without accompanying luminous discharge, thereby selecting a non-luminous cell.

13. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 1, wherein in conducting said sustaining operation ~~second step~~, onto either one or both of said first and second electrodes is applied the first sustain pulse voltage, corresponding thereto respectively, and as the sustain pulse voltage following after the second one is applied sustain pulse voltage, being narrower in pulse width than that of said first sustain pulse voltage.

14. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 2, wherein in conducting said sustaining operation ~~second step~~, onto either one or both of said first and second electrodes is applied the first sustain pulse voltage, corresponding thereto respectively, and as the sustain pulse voltage following after the second one is applied sustain pulse voltage, being narrower in pulse width than that of said first sustain pulse voltage.

15. (Currently amended) A driving method for a plasma display panel, described in ~~any one of the~~ claim 3, wherein in conducting said sustaining operation ~~second step~~, onto either one or both of said first and second electrodes is applied the first sustain pulse voltage, corresponding thereto respectively, and as the sustain pulse voltage following after the second one is applied sustain pulse voltage, being narrower in pulse width than that of said first sustain pulse voltage.

16. (Currently amended) A driving method for a plasma display panel having an address electrode, a first display electrode formed on said address electrode, a second display electrode formed on a surface opposing to said the first display electrode, and a partition wall formed between said first display electrode and said second display electrode and including a metal electrode therein, comprising ~~the following steps:~~

~~a first step for conducting all write-in, respectively, by plural numbers of sub-fields to conduct all write-in, respectively;~~

~~a second step for conducting an addressing operation;~~

~~a third step for conducting a sustaining operation; and~~

~~a fourth step for conduction conducting an erase operation, wherein,~~

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in conducting said all write-in first step, a wall charge is formed through initial discharge caused by applying pulse voltages onto said-address electrode and said first display electrode, respectively, and by causing self-erase discharge after said pulse voltages are removed, wall charge is formed by applying voltages onto said address electrode and said first display electrode, respectively;

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in conducting said addressing operation second step, an address pulse voltage is applied onto said address electrode upon basis of the picture signal, nearly in synchronism with scan pulse voltage onto said first display electrode, so as to remove said wall charge without accompanying luminous discharge, thereby selecting a non-luminous cell(s);

in conducting said sustaining operation third step, onto a luminous cell(s) selected through forming said wall charge, short-pulse voltage is applied onto said address electrode and sustain pulse voltage onto said first display electrode, so as to cause pre-discharge, and thereafter, by means of sustain pulse voltages applied onto said first display electrode and said second display electrode alternately, display luminous discharge is repeated through the initial discharge between said metal electrode grounded to the earth, thereby applying a last sustain pulse voltage onto said second display electrode; and

in conducting said erase operation fourth step, only onto said first display electrode, or onto said first display electrode and said address electrode, respectively, thin-line

short pulse voltage is applied, thereby causing discharge for erasing the wall charge between said metal electrode, said address electrode, and said second display electrode.

17. (Currently amended) A driving method for a plasma display panel, as described in the claim 16, wherein, in conducting said all write-in first step, onto said address electrode and said first display electrode are applied the short-pulse voltages being different in polarity for generating the space charge through the initial discharge and long-pulse voltages being different in polarity for forming the wall charge, respectively, generating self-erase discharge after removal of said long-pulse voltages, voltages are applied onto said address electrode and said first display electrode, respectively, thereby forming the wall charge.

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18. (Currently amended) A driving method for a plasma display panel, as described in the claim 17, wherein, in conducting said all write-in first step, a sum of the voltages, being applied onto said address electrode and said first display electrode, respectively, in absolute value thereof, is made larger in case of said short-pulse voltage than that in case of said long-pulse voltage.

19. (Currently amended) A driving method for a plasma display panel, as described in the claim 17, wherein at least one of said plural numbers of sub-fields causes the space charge with using said short-pulse voltage in conducting said all write-in first step, while in remaining sub-field(s) using no such the short-pulse voltage is shared in common the space charge, which is caused by said thin-line short pulse voltage applied only onto said first display electrode, or onto said first display electrode and said address electrode, respectively, in conducting said erase operation fourth step.

20. (Currently amended) A driving method for a plasma display panel, as described in the claim 16, wherein in conducting said sustaining operation third step, during repetition of the display luminous discharge through the initial discharge between said metal electrode grounded to the earth by means of the sustain pulse voltage, being applied onto said first display electrode and said second display electrode, alternately,

onto said second display electrode is applied pulse voltage, being different in polarity and nearly in synchronism with the sustain pulse voltage to be applied onto said first display electrode, thereby forming the space charge generated after the discharge between said address electrode or said metal electrode and said first display electrode, in form of wall charge on said second display electrode, and

onto said first display electrode is applied pulse voltage, being different in polarity and nearly in synchronism with the sustain pulse voltage to be applied onto said second display electrode, thereby forming the space charge generated after the discharge between said second display electrode and said address electrode, in form of wall charge on said first display electrode.

21. (New) A plasma display apparatus, comprising:

a front substrate;⁽⁶⁾

a reverse-side substrate;⁽¹⁵⁾

a partition wall;⁽¹⁵⁾

dielectric layers,^(8,9,10,14) each being formed on said front surface, said reverse side surface and said partition wall, respectively;

a fluophor layer, being formed within a region surrounded by said front surface, said reverse-side surface and said partition wall; and

electrodes for bringing said fluophor layer to generate ultraviolet rays therefrom, including an address electrode, a first electrode crossing over said address electrode, a second electrode, and a third electrode, wherein

a first period for conducting an addressing operation for each sub-field and a second period for conducting a sustaining operation for display upon basis of a result of said addressing,

during said first period, an electric potential is generated between said address electrode and said first electrode;

during said second period, a sustain pulse voltage is generated across one of those electrodes, being defined between said first electrode and said second electrode or between said

second electrode and said third electrode, in a form of potential difference therebetween, while generating a potential difference upon other of those electrodes, being reverse to polarity of said sustain pulse voltage, during said sustain pulse voltage is generated.

22. (New) The plasma display apparatus, as defined in the claim 21, wherein the potential difference is generated between said first electrode and said second electrode and between said second electrode and said third electrode, alternately, in the form of said sustain voltage.

23. (New) The plasma display apparatus, as defined in the claim 21, wherein a pulse is applied onto said address electrode, being same in polarity to that which is applied onto said first electrode, during the period when the potential difference is generated between said first electrode and said second electrode in the form of said sustain pulse voltage.

24. (New) The plasma display apparatus, as defined in the claim 21, wherein a wall charge is increased due to the potential difference, which is generated between said second electrode and said third electrode.

25. (New) The plasma display apparatus, as defined in the claim 21, wherein said second electrode is a metal partition wall, on a side surface of which is formed a dielectric layer.

26. (New) The plasma display apparatus, as defined in the claim 21, wherein during said second period:

a negative sustain pulse voltage is applied onto said first electrode and said second electrode, alternately, while conduction an anode drive upon said second electrode; and

a positive voltage is applied onto other electrode, on which no said sustain pulse voltage is applied, during the period when said sustain pulse voltage is applied onto either one of said first electrode and said third electrode.

27. (New) The plasma display apparatus, as defined in the claim 21, wherein during said second period:

a negative sustain pulse voltage is applied onto said first electrode;
an anode drive is conducted upon said second electrode; and
a positive voltage is applied onto said third electrode during a period when said sustain pulse voltage is applied onto said first electrode.

28. (New) The plasma display apparatus, as defined in the claim 21, wherein during said second period:

a first sustain pulse of negative sustain pulse voltage is applied onto said first electrode;

an anode drive is conducted upon said second electrode; and

a positive voltage is applied onto said third electrode during a period when said sustain pulse voltage is applied onto said first electrode.

29. (New) The plasma display apparatus, as defined in the claim 21, wherein during said second period:

sustain pulse voltages including a second one and those following thereafter are applied onto said first electrode as the sustain pulses thereof;

an anode drive is conducted upon said second electrode; and

a positive voltage is applied onto said first electrode during a period when said sustain pulse voltage is applied onto said third electrode.
